x86-64 Memory Operand Addressing Forms

**Addressing Form Example**

BaseReg mov rax, [rbx]

BaseReg + Disp mov rax, [rbx+16]

IndexReg \* SF + Disp mov rax, [r15\*8+48]

BaseReg + IndexReg mov rax, [rbx+r15]

BaseReg + IndexReg + Disp mov rax, [rbx+r15+32]

BaseReg + IndexReg \* SF mov rax, [rbx+r15\*8]

BaseReg + IndexReg \* SF + Disp mov rax, [rbx+r15\*8+64]

RIP + Disp mov rax, [Val]

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SF = ScaleFactor = Factor de Escala

Disp = displacement = desplazamiento